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In RF Microelectronics, Second Edition, Behzad Razavi systematically teaches the fundamentals as well as the state-of-the-art developments in the analysis and design of RF circuits and transceivers. Razavi has written the second edition to reflect today's RF microelectronics, covering key topics in far greater detail.

### Rf Microelectronics (Prentice Hall Communications ...

RF MICROELECTRONICS Second Edition. RF MICROELECTRONICS Second Edition Behzad Razavi Upper Saddle River, NJ •Boston Indianapolis • San Francisco New York •Toronto Montreal London •Munich Paris • Madrid Capetown •Sydney Tokyo Singapore • Mexico City.

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### Design of Analog CMOS Integrated Circuits Second Edition

The use of a superheterodyne technique entails several trade-offs. Image rejection is a prevailing concern in this architecture. During the first down-conversion to IF, any unwanted activity at a frequency spaced at  $f_{IF}$  offset from the LO frequency ( $f_{LO}$ ) on the opposite side of  $f_{LO}$  from the desired RF channel, will produce a mixing product falling right into the down-converted channel at  $f_{IF}$ .

### On the Direct Conversion Receiver -- A Tutorial ...

1. Behzad Razavi, "Design of Analog CMOS Integrated Circuits," McGraw Hill Education; Second edition (1 November 2017), ISBN-13: 978-93870678442. Charles A. Desoer & Ernest S. Kuh, "Basic Circuit Theory," McGraw-Hill Book Company, 1969.

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B. Razavi, RF Microelectronics, Prentice Hall, 1998. K. Shu et E. Sánchez-Sinencio, CMOS PLL Synthesizers: Analysis and Design, Springer, 2004. Predicting the Phase Noise and Jitter of PLL-Based Frequency Synthesizers

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